SDR Forum Workshop on Rapid Prototyping and Development
17 September 2007
Crowne Plaza, Cherry Hill NJ

The SDR Forum will host a workshop on the use of SDR technologies in rapid prototyping. Key players in the SDR market will be presenting at this workshop detailing how their products can be used by the wireless community to accelerate the development cycle for new wireless technologies and speed time to market or time to deployment for advanced wireless systems and networks for commercial, public safety, and defense users. Key presentations that will be given at this workshop include:

Time	Company	Presenter	Title
8:00 - 8:30		TBD	Introductory Comments
8:30 - 9:30	Mathworks	John Irza	Model based design using Simulink and Real Time Workshop
9:45-10:45	Xilinx	Chad Epifanio	Rapid Prototyping in Support of Spiral Development
10:45 - 11:45	Bittware	Dave Rupe	Rapid Prototype Development of FPGA Systems Using Software Reprogrammable Reconfiguration: The Path to a Complete SDR Solution
11:45 - 1:00	Lunch		
1:00 - 2:00	Spectrum Signal Processing	Cyrus Sy	Using 3U CompactPCI for Quick Response C4ISR Systems
2:00 - 3:00	Zeligsoft	Neil Patterson	Integrated System Centric Development - The key to rapid evolution from prototype to production of SDR Systems
3:15 - 4:15	Lyrtech	Louis Belanger	Rapid prototyping of waveforms using the SFF (Small-Form Factor)SDR DP (Development Platform)
4:15 - 5:15	Anritsu	Eric Hakanson	Rapid SDR Prototype Hardware Test using Flexible RF Instruments
5:15 - 6:00	Synplicity	Shiv Balakrishnan	Waveform Modeling for Portability and Efficient Implementation

This workshop will be held in conjunction with the 55th General Meeting of the SDR Forum to be held in Cherry Hill, New Jersey from September 18th through 20th. Additional details on this meeting, including a complete meeting agenda, can be found at http://www.sdrforum.org/pages/whatsNew/whatsNewDetails.asp?id=385&news_cat=Forum+Meetings